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Kata et al.

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#### [54] PROCESS FOR MANUFACTURING SEMICONDUCTOR DEVICE AND SEMICONDUCTOR WAFER

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### [56] References Cited

#### U.S. PATENT DOCUMENTS

4,604,644 8/ 5,027,188 6/ 5,137,845 8/ 5,250,843 10/ 5,434,452 7/	/1986 Beckhan /1991 Owada e /1992 Lochon /1993 Eichelbe /1995 Higgins,	
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#### FOREIGN PATENT DOCUMENTS

0485760 A1	5/1992	European Pat. Off
4952973	5/1974	Japan .
5129366	5/1993	Japan .
677293	3/1994	Japan .

#### OTHER PUBLICATIONS

R. Chanchani et al.; "A New mini Ball Grid Array (mBGA) Multichip Module Technology"; International Journal of Microcircuits & Electronic Packaging, 18(1995) Third Quarter, No. 3, Reston, VA, pp. 185–192., Dec. 1995.

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#### ABSTRACT

A process for manufacturing a semiconductor device includes defining chip sections on a wafer by scribe lines with each chip section having chip electrodes formed thereon. The wafer is covered with a passivating film except for on the chip electrodes. Aluminum interconnection layers are provided such that each layer is connected to the chip electrode at one end thereof and the other end of the layer is extended towards the central portion of the chip section. A cover coating film is applied on the passivating film and the layers. A number of apertures are formed in the coating film passing therethrough, and bump electrodes are formed at the position corresponding to the apertures. The chip sections are then separated from each other along the scribe lines into semiconductor devices.

#### 12 Claims, 4 Drawing Sheets

